

FIG. 2

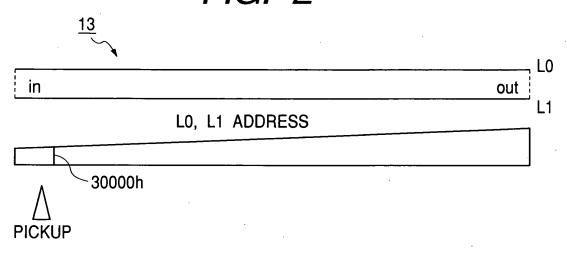
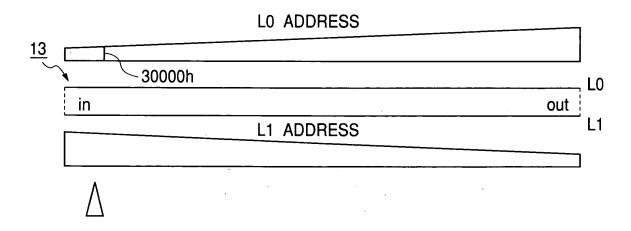
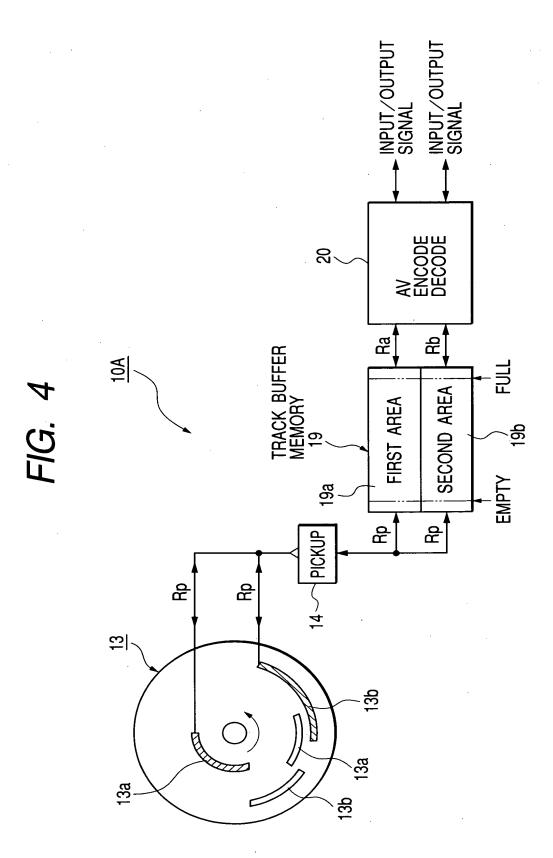
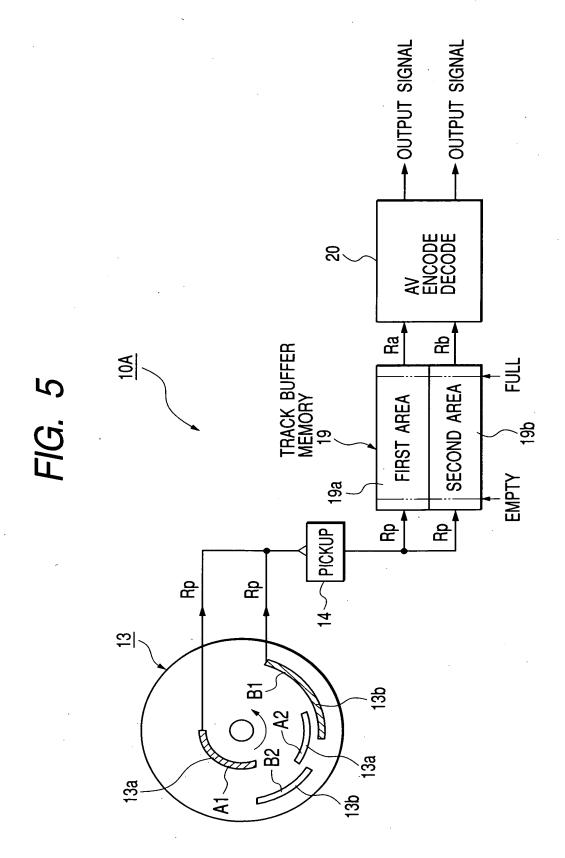


FIG. 3







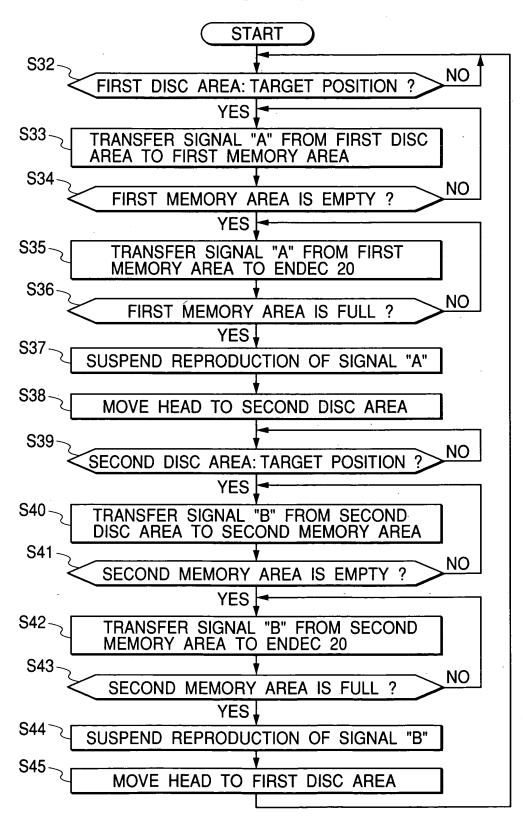
F/G. 6

	A1	A2	A 3	A4	A5	A6	A7	A8
FIRST AREAS 13a	A9	A10	A11	A12	A13	A14	A15	A16
	147	α V						

FIG 7

COND	COND AREAS	136	B1	B2	B3	B4	B5	98	87	B8
			68 68	B10	B11					

FIG. 8



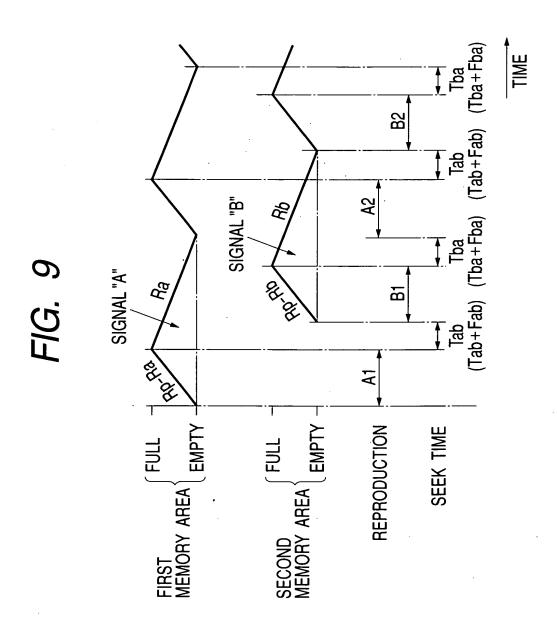


FIG. 10

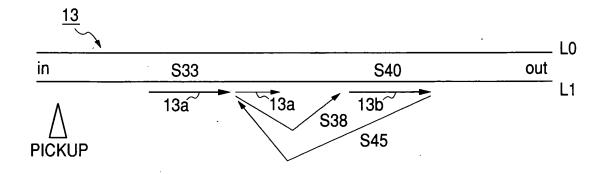
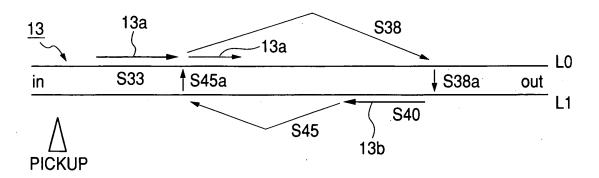


FIG. 11



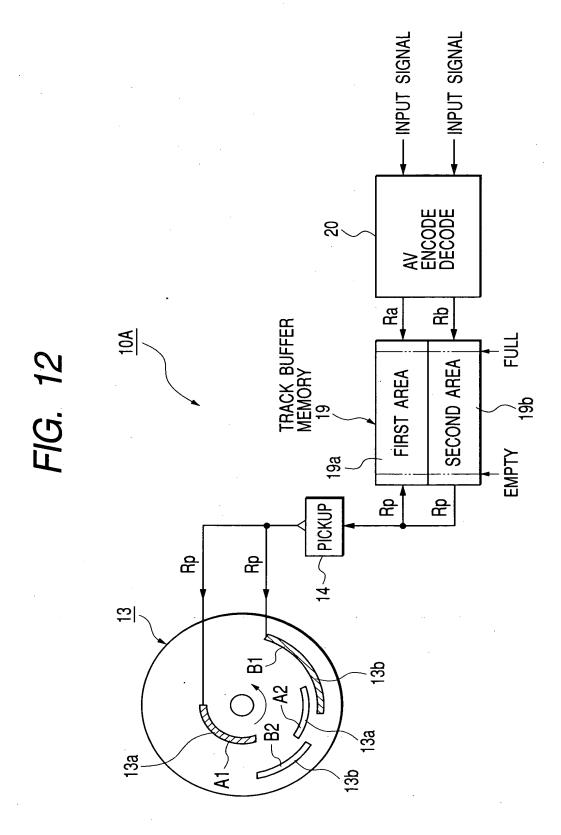
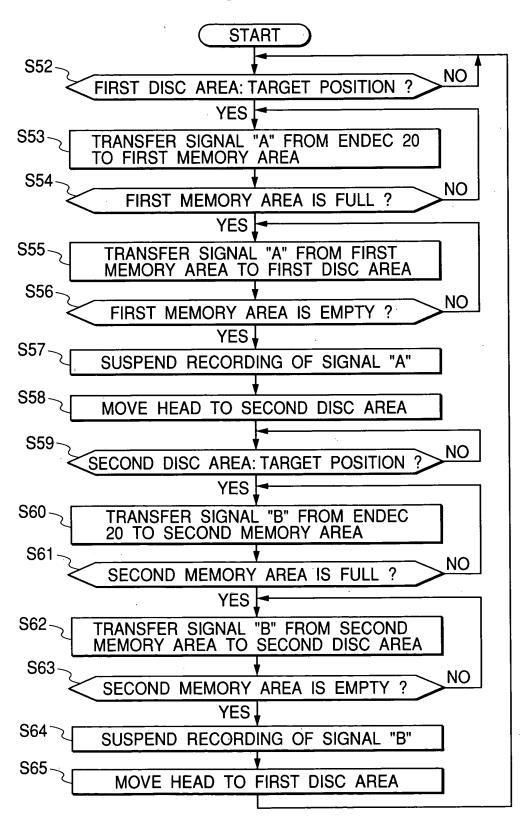
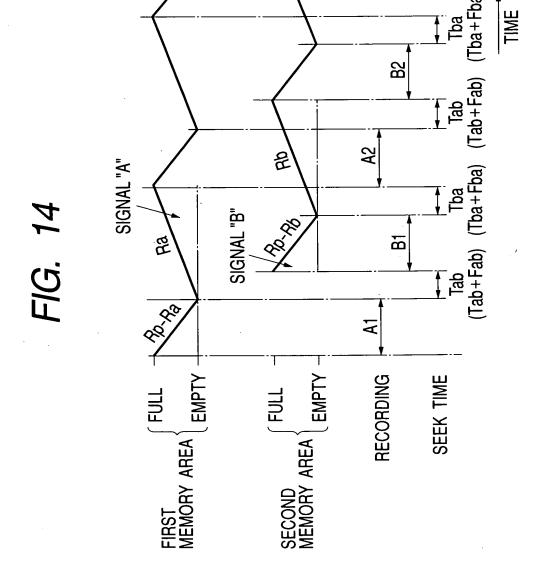


FIG. 13





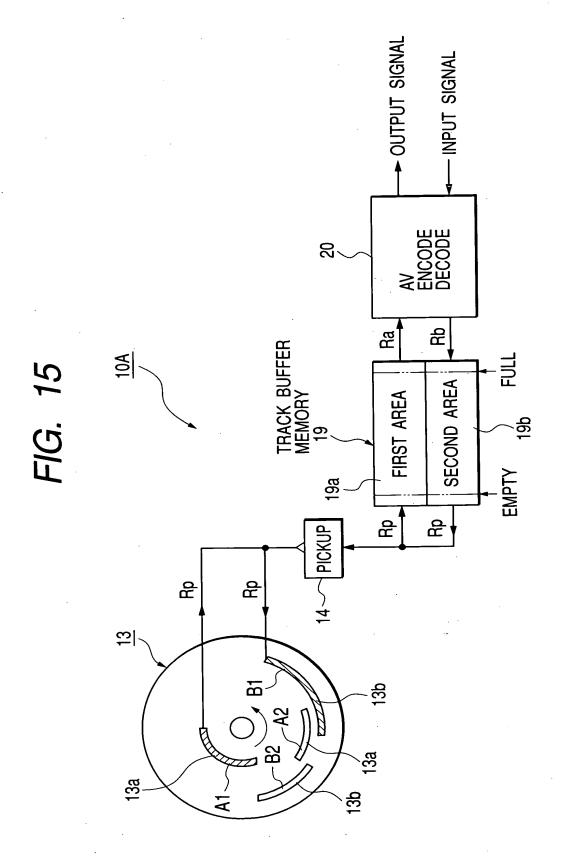
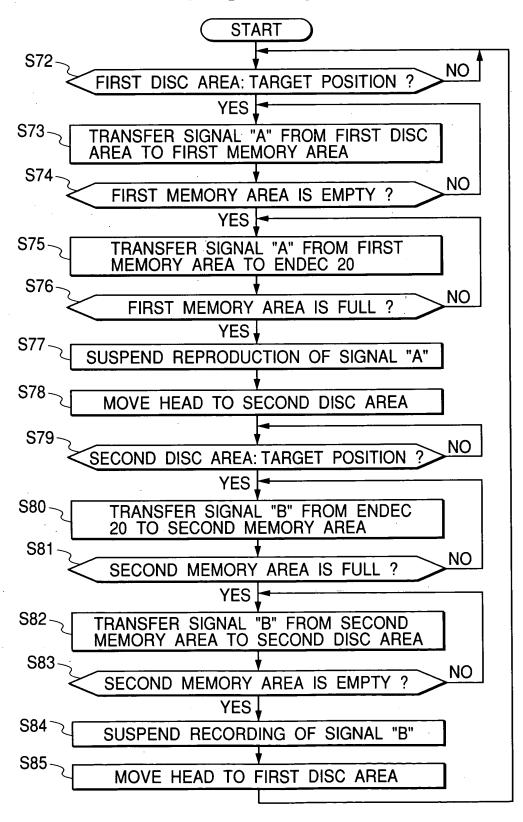
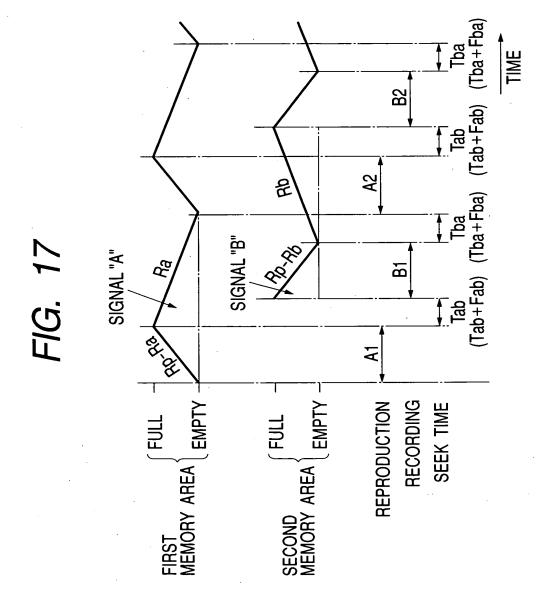
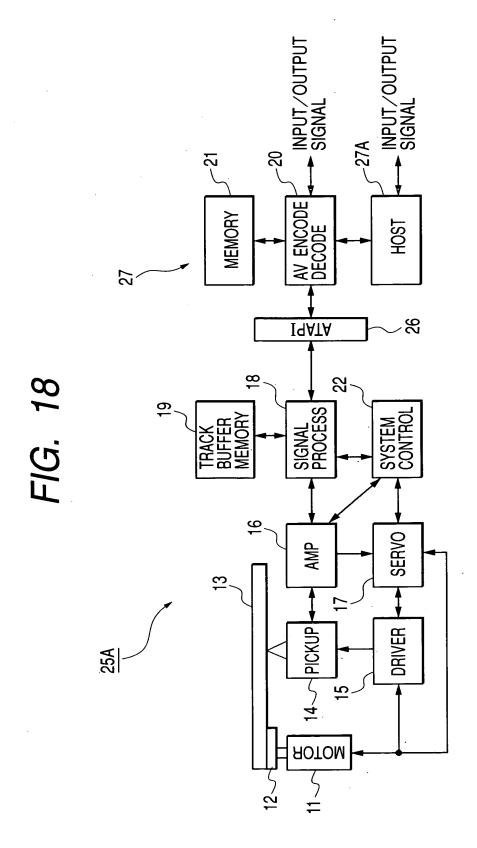


FIG. 16







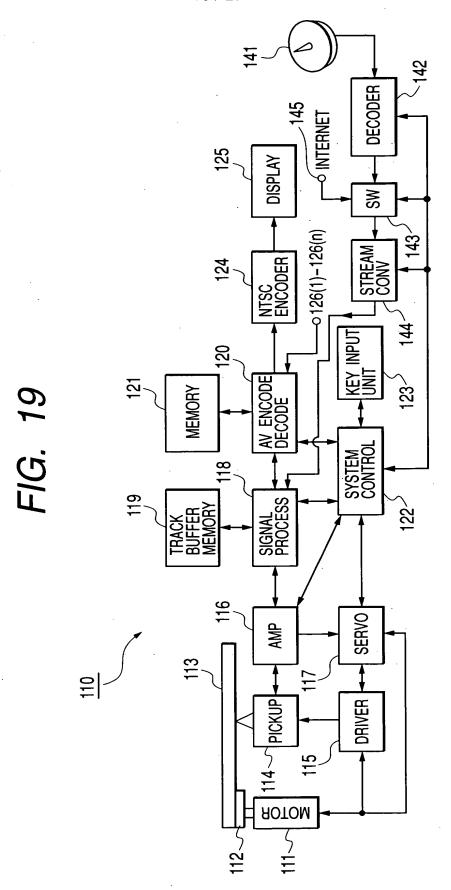


FIG. 20

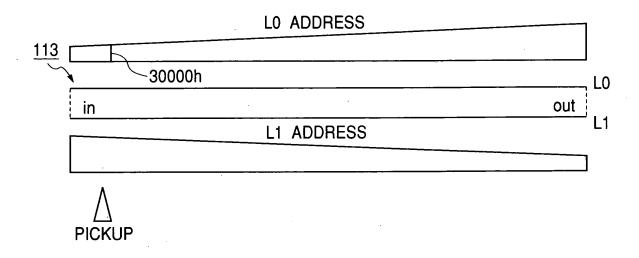
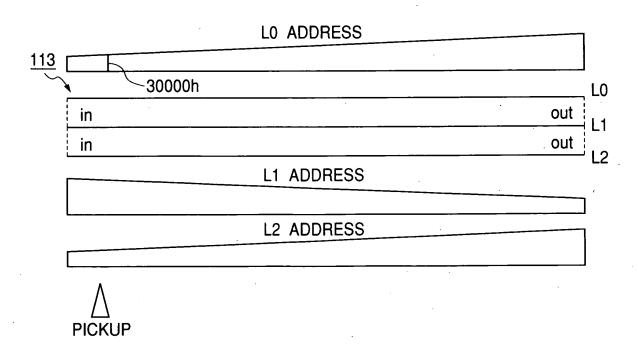


FIG. 21



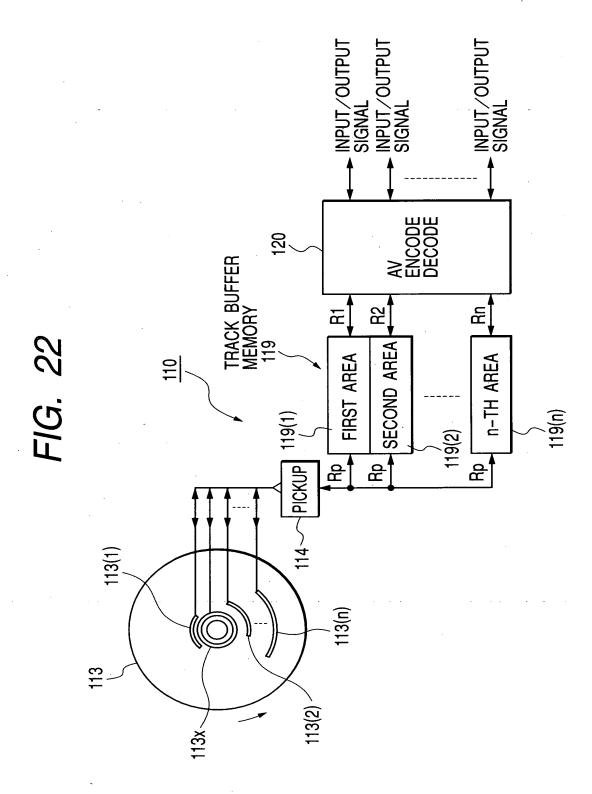


FIG. 23

	A 1	A2	A 3	A4	A5	A 6	A 7	A8
113(1)	A 9	A10	A11	A12	A13	A14	A15	A16

FIG. 24

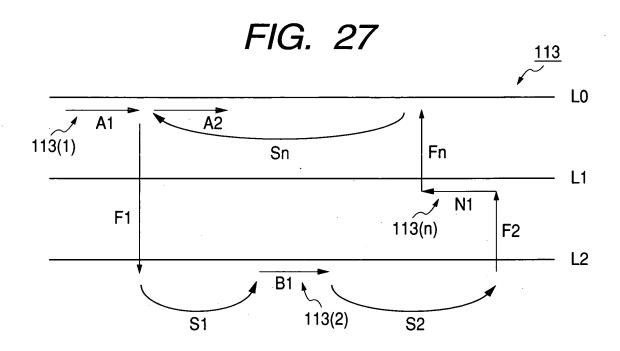
113(2) 🕶 B1 B2	B3	B4	B5	B6	B7	B8	
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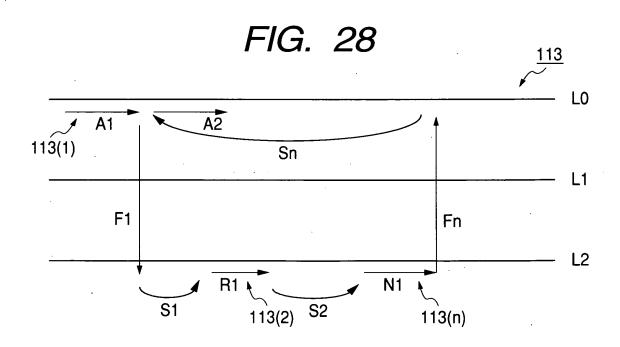
FIG. 25

440/~\ -	N1	N2	N3	N4	N5	N6	N7	N8 [°]
113(n) ~-	N9	N10	N11				V	

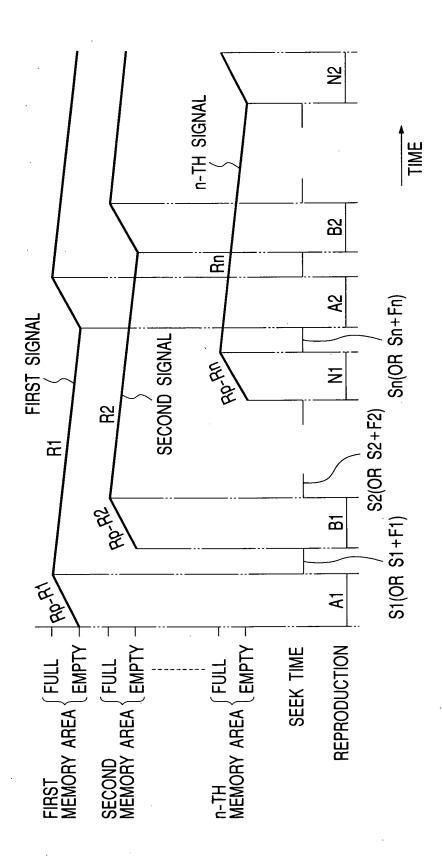
FIG. 26

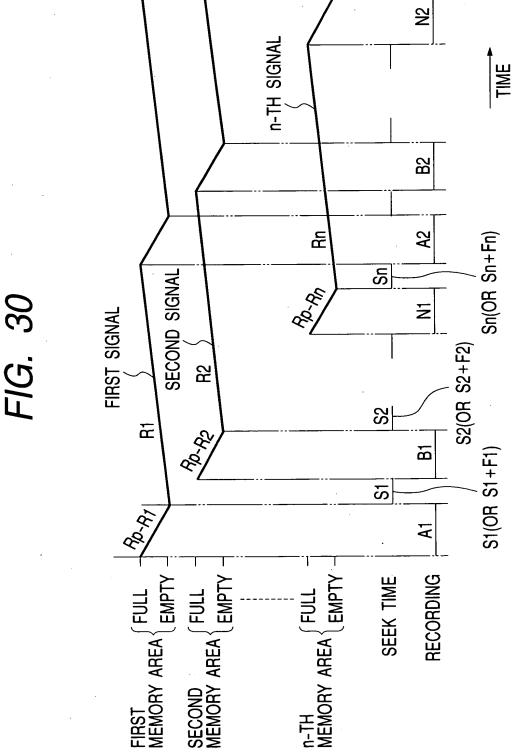
113x → X1	X2	Х3	X4	X5	X6	X7	X8	
		1	ı					











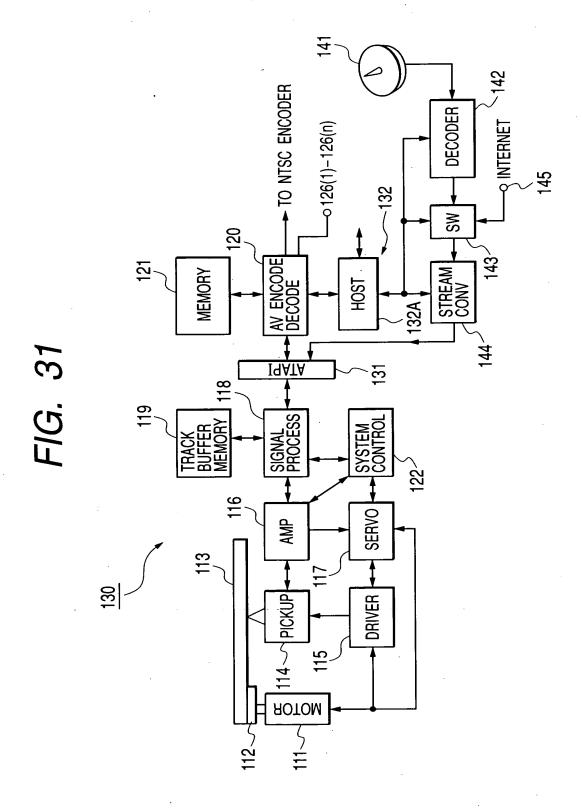


FIG. 32

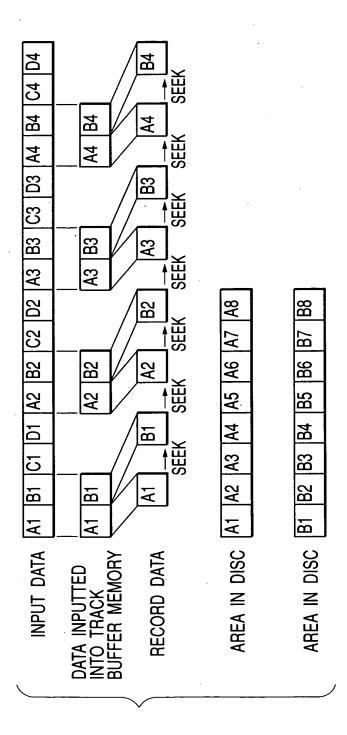
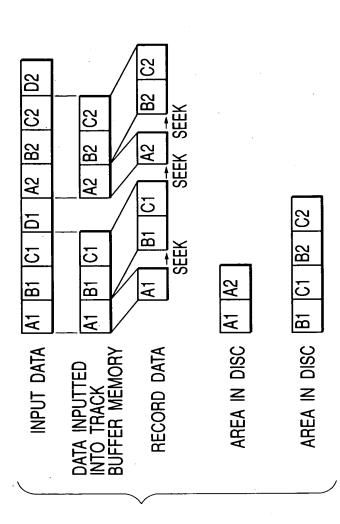


FIG. 33



F1G. 34

			D4	•		
D4	D4		A1 B1 C1 D1 A2 B2 C2 D2 A3 B3 C3 D3 A4 B4 C4 D4		D4	
2			B4		5	
B4	A1 B1 C1 D1 A2 B2 C2 D2 A3 B3 C3 D3 A4 B4 C4 D4		A4		B4	
A4			D3		A4	
<u>B3</u>	<u> </u>		င္သ		<u> </u>	
೮	<u>ප</u>		B3		ဌ	
83	83		A3		B3	
A3			D2		A3	
D2	D2		C5		<u>D</u> 2	
22			B2		C5	
B2	B2		A2		B2	
A2	A2		<u>D</u>		A2.	
5	<u> </u>		Cl		<u></u>	
2	2		B1		ರ	l
<u>B</u>	<u>8</u>		A1		<u>B</u> 1	
A1					A 1	
INPUT DATA A1 B1 C1 D1 A2 B2 C2 D2 A3 B3 C3 D3 A4 B4 C4 D4	DATA INPUTTED INTO TRACK	BUFFER MEMORY	RECORD DATA		AREA IN DISC A1 B1 C1 D1 A2 B2 C2 D2 A3 B3 C3 D3 A4 B4 C4 D4	
						_

F/G. 35

